

AMENDMENT

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Please replace the claims with the following:

1 1. (Thrice Amended) A method for compressing video data in a computer
2 system, comprising:
3 receiving a stream of data from a current video frame in the computer
4 system;
5 computing a difference frame from the current video frame and a previous
6 video frame as the current video frame streams into the computer system, wherein
7 computing the difference frame includes computing the difference frame in a core
8 logic chip within the computer system, wherein the core logic chip is a north
9 bridge chip that couples the processor to a main memory and a system bus for the
10 computer system; and
11 storing the difference frame in a memory in the computer system.

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1 2. (Unchanged) The method of claim 1, including storing the current video
2 frame in the memory in the computer system.

1 3. (Unchanged) The method of claim 2, wherein the current video frame is
2 written over a previous video frame in the memory.

1 4. (Unchanged) The method of claim 1, wherein computing the difference
2 frame includes computing an exclusive-OR between the current video frame and
3 the previous video frame.

1 5. (Unchanged) The method of claim 1, wherein computing the difference
2 frame includes computing a difference between a block of data from the current
3 video frame and a block of data from the previous video frame.

1 6. (Unchanged) The method of claim 1, wherein storing the difference
2 frame in memory includes storing the difference frame in the memory using block
3 transfers.

1 7. (Unchanged) The method of claim 1, including compressing the video
2 data using the difference frame to produce compressed video data.

1 8. (Unchanged) The method of claim 1, including performing a color space
2 conversion on the video data.

1 9. (Unchanged) The method of claim 1, including using the video data in
2 compressed form in a video teleconferencing system.

1 10. (Unchanged) The method of claim 1, including storing instructions and
2 data for the computer system in the memory.

Claim 11 was previously cancelled.

1 12. (Unchanged) The method of claim 1, wherein computing the
2 difference frame includes computing the difference frame in circuitry outside of a
3 central processing unit in the computer system.

1 13. (Thrice Amended) A method for compressing video data in a computer
2 system, comprising:

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3 receiving a stream of data from a current video frame in the computer
4 system;
5 computing a difference frame from the current video frame and a previous
6 video frame as the current video frame streams into the computer system, wherein
7 computing the difference frame includes computing an exclusive-OR between the
8 current video frame and the previous video frame, and wherein computing the
9 difference frame includes computing the difference frame in a core logic chip
10 within the computer system, wherein the core logic chip is a north bridge chip that
11 couples the processor to a main memory and a system bus for the computer
12 system;
13 storing the difference frame in a memory in the computer system;
14 storing the current video frame in the memory in the computer system; and
15 compressing the video data using the difference frame to produce
16 compressed video data.

1 14. (Unchanged) The method of claim 13, wherein the current video frame
2 is written over a previous video frame in the memory.

1 15. (Unchanged) The method of claim 13, wherein computing the
2 difference frame includes computing a difference between a block of data from
3 the current video frame and a block of data from the previous video frame.

1 16. (Unchanged) The method of claim 13, wherein storing the difference
2 frame in memory includes storing the difference frame in the memory using block
3 transfers.

1 17. (Unchanged) The method of claim 13, including using the compressed
2 data in a video teleconferencing system.

1 18. (Unchanged) The method of claim 13, including performing a color
2 space conversion on the video data.

1 19. (Unchanged) The method of claim 13, including storing instructions
2 and data for the computer system in the memory.

Claim 20 was previously cancelled.